

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/798,138	03/11/2004	Andy Yu	20359.2 5554	
49358 75	49358 7590 04/05/2006		EXAMINER	
CARLTON FIELDS, PA 1201 WEST PEACHTREE STREET			VU, DAVID	
	ANTIC CENTER	ART UNIT	PAPER NUMBER	
ATLANTA, GA 30309			2818	
			DATE MAILED: 04/05/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

Λ	
rtx	
١K١	
-uv	
	(A)

	Application No.	Applicant(s)				
	10/798,138	YU ET AL.				
Office Action Summary	Examiner	Art Unit				
	DAVID VU	2818				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	TE OF THIS COMMUNICATION 6(a). In no event, however, may a reply be tim ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	l. ely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 09 Ja	nuary 2006.					
,— ,	action is non-final.					
3) Since this application is in condition for allowan		secution as to the merits is				
closed in accordance with the practice under E.	·					
Disposition of Claims						
4) Claim(s) 1-40 is/are pending in the application.						
4a) Of the above claim(s) is/are withdraw	n from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-40</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers						
9) The specification is objected to by the Examiner.						
10)⊠ The drawing(s) filed on 11 March 2004 is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
Applicant may not request that any objection to the	frawing(s) be held in abeyance. See	37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119	·					
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list of 	have been received. have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No In this National Stage				
Attachment(s)						
Notice of References Cited (PTO-892)	4) Interview Summary					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 06/20/05.	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	atent Application (PTO-152)				

Application/Control Number: 10/798,138

Art Unit: 2818

DETAILED ACTION

Election/ Restriction

1. Applicant's election of Species I (Claims 1-40) filed on 01/09/06 is acknowledged. The traverse is on the ground(s) that "the memory cells and memory arrays are not independent of each other". The examiner withdraws the species requirement, thus all of the device claims 1-40 are examined as follows.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-6, 8-26 and 28-40 are rejected under 35 U. S. C. 102(e) as being anticipated by Hung et al. (US 6,765,260, herein after Hung).

Regarding claims 1-4, 6, 21-24 and 26, Hung discloses in figs. 3G-3H an electrically erasable programmable memory device, comprising: a first semiconductor layer 200 doped with a first dopant in a first concentration (p-type); a second semiconductor layer 202, adjacent the

Art Unit: 2818

first semiconductor layer 200, doped with a second dopant that has an opposite electrical characteristic than the first dopant (n-type), the second semiconductor layer having a top side; two spaced-apart diffusion regions 212/216 embedded in the top side of the second semiconductor layer, each diffusion region doped with the first dopant in a second concentration greater than the first concentration (col. 6, lines 4-25), the two diffusion regions including a, first diffusion region 218a/218b and a second diffusion region 216, a first channel region defined between the first diffusion region 218a/218b and the second diffusion region 216, a floating gate 214a/214b, comprising a conductive material, disposed adjacent the first diffusion region 218a/218b and above the first channel region and separated therefrom by a first insulator region 212, the floating gate 214a/214b capable of storing electrical charge and having at least two lateral sides; and a control gate 208a/208b, comprising a conductive material, disposed laterally adjacent the floating gate 214a/214b and surrounding at least two sides of the floating gate (see fig. 6) and separated therefrom by a vertical insulator layer 212, the control gate 208a/208b being disposed above the first channel region and separated therefrom by a second insulator region 206a/206b.

Regarding claims 5 and 25, Hung discloses the first insulator region 212 having a thickness of about 90 angstroms (col. 5, lines 35-39).

Regarding claims 8 and 28, Hung discloses the floating gate 214a/214b and the control gate 208a/208b are wrapped by a spacer 212/210/220 (fig. 3H).

Regarding claims 9, 15, 29 and 35, Hung discloses the second diffusion 216 is in contact with a vertical connector 230, the vertical connector 230 being separated from the control gate 208a/208b by a second vertical insulator 212 (fig. 3H).

Art Unit: 2818

As for the recitation that "wherein charge is transported from the first channel region to the floating gate when a first combination of voltages is applied....." (claims 10-14, 16-20, 30-34 and 36-40), it refers to an operational limitation and any such limitation must distinguish from the prior art in terms of structure rather than function, *In re Schreiber*, 128 F.3d 1473, 1477-78, 44 USPQ2d 1429, 1431-32 (Fed. Cir. 1997); See also *In re Swinehart*, 439 F.2d210, 212-13, 169 USPQ 226, 228-29 (CCPA 1971; *In re Danly*, 263, F.2d 844, 847, 120 USPQ 528, 531 (CCPA 1959).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 7 and 27 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Hung 3. (US 6,765,260) in view of Hong (US Pat. 5,427,968).

Hung fails to disclose the first vertical insulator is an ONO structure. However, Hong teaches in col. 3, lines 63-68 that the vertical insulator layer 64 is formed of ONO (figs. 4c-4d). It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the invention of Hung by forming the ONO layer as taught by Hong since the material such as oxide, NO or ONO layer is recognized equivalent material for forming the dielectric layer in a semiconductor device.

Conclusion

Any inquiry concerning this communication or earlier communications from the 4. examiner should be directed to David Vu whose telephone number is (571) 272-1798. The examiner can normally be reached on Monday-Friday from 8:00am to 5:00pm. If attempt to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR, Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

Application/Control Number: 10/798,138

Art Unit: 2818

Page 6

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DAVID VU